

FARHANA ZAMAN

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OBJECTIVE:

An internship position in the field of microelectronics

EDUCATION:

2006-present **Georgia Institute of Technology**, Atlanta GA Expected Graduation: Aug. 2008
Ph.D. in Electrical and Computer Engineering
Minor: Materials Science and Engineering
Cumulative GPA: 3.70/4.00

2004-2006 **Georgia Institute of Technology**, Atlanta GA
M.S. in Electrical and Computer Engineering
Cumulative GPA: 3.70/4.00
Courses taken include: Microelectronics Technologies, Microelectronics System Packaging, Theory of MEMS, Surface Analysis, Nanomaterials and Nanotechnology

2000-2004 **Georgia Institute of Technology**, Atlanta GA
B.S. in Electrical Engineering
Cumulative GPA: 3.69/4.00
Courses taken include: Microelectronic Circuits, Integrated Circuits Fabrication, Semiconductor Devices

SKILLS:

Computer:

AutoCAD, MATLAB, MathCAD, PSpice Simulation, LabVIEW, MS Office, Adobe Photoshop

Foreign Languages:

English, Bengali, Hindi, Urdu, French, Arabic

Management Training:

Principles of Management for Engineers

WORK EXPERIENCE:

2004-present **Georgia Institute of Technology**, Atlanta GA
Graduate Research Assistant, School of Electrical and Computer Engineering

- Working under Dr. James Meindl in the Gigascale Integration Group
- Operating a cutting-edge electron-beam lithography system to fabricate sub-5 nm electrode gaps which can be applied to DNA detection and chemical sensing
- Managing projects as a consultant on the electron-beam lithography system for researchers outside the Georgia Institute of Technology

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2003-2004 **Georgia Institute of Technology, Atlanta GA**
Undergraduate Research Opportunities Program

- Gained research experience as an undergraduate working under Dr. Oliver Brand who specializes in MEMS and microsensors
- Developed measurement and control applications that interface with various lab apparatus, such as the oscilloscope, using LabVIEW

2001-2004 **Georgia Institute of Technology, Atlanta GA**
Clean room Coop, Microelectronics Research Center

- Gained expertise in operating processing equipment, such as the PECVD, ICP, and evaporators
- Worked with technical staff in the maintenance of the processing equipment
- Optimized processing steps on thin film equipment by conducting deposition and etching experiments
- Trained researchers on how to operate clean room processing and metrology equipment, such as metal and dielectric sputtering systems, lithography tools, and contact stylus profilers
- Initiated daily status reports detailing important clean room related announcements
- Authored proper clean room policies and trained researchers about them
- Created a successful online quiz for incoming researchers to test their knowledge in clean room etiquette in order to ensure a safe working environment

HONORS AND ACTIVITIES:

Graduate Woman's Fellowship for Leadership in Microelectronics, Texas Instruments

Graduation with Highest Honors, Georgia Institute of Technology (2004)

Best Coop of the Year Award, Microelectronics Research Center (2002-2003)

Clean room Trainer Award, Microelectronics Research Center

Clean room Mentor for National Nanotechnology Infrastructure Network

General Secretary and Vice President, Bangladesh Student Association (2002-2004)

Member, Eta Kappa Nu, National ECE Honor Society

Member, Institute of Electrical and Electronics Engineers

Member, Nano@Tech, Georgia Tech Group for Nanotechnology

Volunteer, Women's Leadership Conference

REFERENCES:

Available upon request