

Daniel Nadeau

dnadeau3@gatech.edu
dnadeau13@gmail.com

Permanent Address:
314 Windy Ridge Ln.
Atlanta, GA 30339
770-826-2986

Objective: Electrical Engineering Design position in multi-media entertainment or defense

Education: **Georgia Institute of Technology, Atlanta, GA** **8/2006 – Present**
(August 2009)

- Candidate for Bachelor of Science in Electrical Engineering
- Current GPA 3.3/4.00

Macon State College, Macon, GA **8/2004 – 5/2006**

- Enrolled in the Regents Engineering Transfer Program
- Overall GPA 3.67/4.00
- Dean's List for every semester enrolled

Central Fellowship Christian Academy, Macon, GA **8/2000 – 5/2004**

- Overall GPA 3.7/4.00
- SAT score of 1290/1600
- Accelerated Math Program

Experience: **Panasonic Automotive Sys. , Peachtree City, GA** **1/2007 – 8/2008**

- Worked as a Co-op for 3 semesters
- Assisted senior engineers in testing and debugging of circuit designs
- Did extensive soldering, including building up entire boards
- Co-designed a Differential to Single Ended Audio Converter and RF Switch Controller
 - Created schematics and layouts, and wrote code in C for RF Switch Controller
- Used Mentor Graphics software to do schematic and board layouts
- Configured vehicles to be used for field testing
- Performed Type Acceptance testing for an XM module
- Used Rhode & Schwarz signal generators, network analyzer, and spectrum analyzer for various testing and characterization

Audio Shop, Warner Robins, GA **8/2004 – 8/2006**

- Planned and installed whole house audio/video systems and home theater systems
- Setup phone/cable/data networks
- Programmed universal RF macro-enabled remotes

Skills: **Hardware:**

- Proficient in phone/cable/data networking
- Soldering ranging from through-hole parts to surface-mounted 40-pin ribbon connectors
- Working knowledge of circuit design

Software:

- Strong knowledge of Microsoft applications: Word, Excel, Power Point
- Mentor Graphics circuit design tools
- National Instruments MultiSim
- Cadence Spice
- Mathcad

Programming: MATLAB, C, C++, basic VHDL