## Transistor-Level Circuit Design $\overline{C} \multimap |$ $\overline{B}$ $\sim$ $\overline{\mathbf{B}}$ $\multimap$ Out Out Out $\overline{c}$ B- $\overline{B}$ -[ $A \dashv [$ $\mathbf{B} \multimap \Big[$ $\overline{D}$ $\multimap$ [ $\overline{\mathbf{D}} \multimap \Big[$ $\overline{\mathbf{B}} \multimap \Big[$ $D^{\multimap}$ C ⊸ - Out Out Out \_ B C-B-B - |D-D- $\overline{c}$ $\overline{D}$ A⊸ [ $\overline{\mathbf{B}}$ - $\circ$ C⊸ $B \multimap \Big[$ $B \multimap$ $C \multimap \Big[$ $D \multimap$



