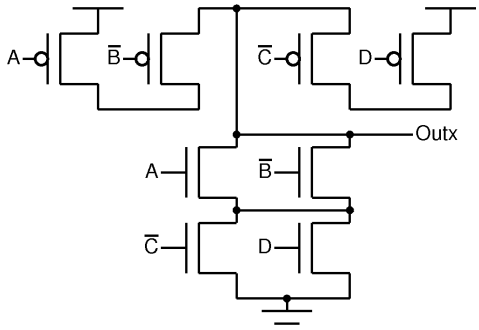
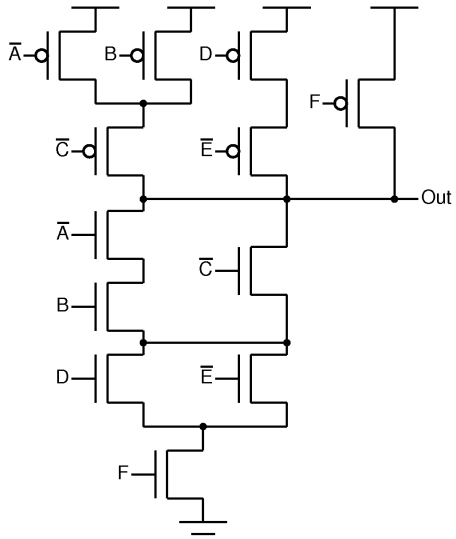
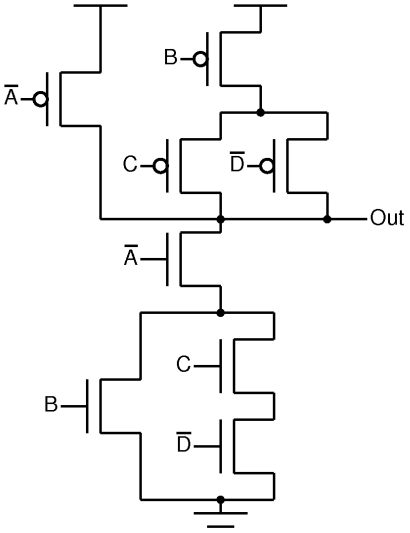
Problem 1 (3 parts, 30 points) Incomplete Circuits

The three parts below contain (A) a pull up network, (B) a pull down network, and (C) an expression to be implemented. For (A) and (B), complete the missing complementary switching networks so the circuit contains no floats or shorts and write the Boolean expression computed by the completed circuit. For (C), design the entire switching network. Assume the inputs and their complements are available.







OUTx = $\overline{A} \cdot B + C \cdot \overline{D}$

OUTy = $(A + \overline{B}) \cdot C + \overline{D} \cdot E + \overline{F}$

OUTz = $A + \overline{B} \cdot (\overline{C} + D)$

Problem 2 (2 parts, 20 points) Boolean Algebra

Part A (12 points) Transform each of the following Boolean expressions to a form where they are ready for switch level implementation (i.e., there should only be bars over input variables, not over operations). The behavior of the expression should remain unchanged. **Do not implement**.

$OUT_X = \overline{\overline{A} + \overline{B \cdot C} + D}$

$OUT_X = A \cdot B \cdot C \cdot \overline{D}$

$OUT_Y = \overline{A \cdot \overline{\overline{B}} \cdot \overline{C \cdot \overline{D} + E}}$

$OUT_Y = (\overline{A} + B) \cdot (\overline{C} + D) \cdot \overline{E}$

Part B (8 points) Derive a canonical sum of products (using minterms) and a product of sums (using maxterms) expression for the truth table below.

A	B	C	$F_{(A,B,C)}$
0	0	0	1
1	0	0	0
0	1	0	1
1	1	0	0
0	0	1	1
1	0	1	1
0	1	1	1
1	1	1	0

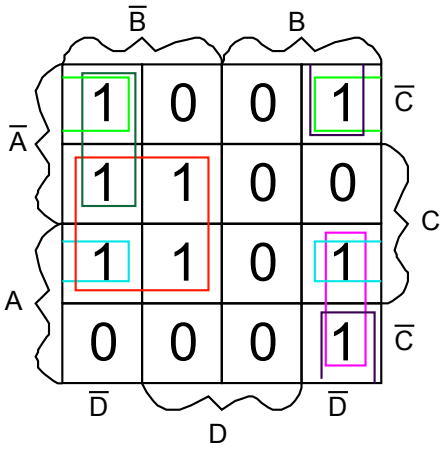
$SOP_{(MINTERMS)} = \overline{A} \cdot \overline{B} \cdot \overline{C} + \overline{A} \cdot B \cdot \overline{C} + \overline{A} \cdot \overline{B} \cdot C + A \cdot \overline{B} \cdot C + \overline{A} \cdot B \cdot C$

$POS_{(MAXTERMS)} = (\overline{A} + B + C) \cdot (\overline{A} + \overline{B} + C) \cdot (\overline{A} + \overline{B} + \overline{C})$

Problem 3 (3 parts, 24 points) Karnaugh Maps

Part A (12 points) For the follow expression, derive a simplified ***sum of products*** expression using a Karnaugh Map. Circle and list *all* prime implicants, indicating which are essential.

$$Out = \overline{A}\cdot\overline{C}\cdot\overline{D} + \overline{A}\cdot\overline{B}\cdot C + A\cdot C\cdot\overline{D} + B\cdot\overline{C}\cdot\overline{D} + A\cdot\overline{B}\cdot C\cdot D$$

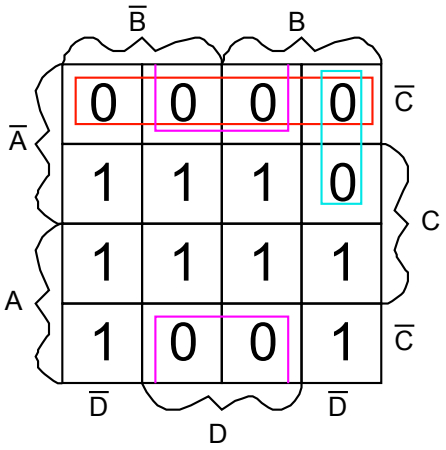


prime implicants	essential? yes	no
$\overline{B}\,C$	☒	☐
$\overline{A}\,\overline{B}\,\overline{D}$	☐	☒
$\overline{A}\,\overline{C}\,\overline{D}$	☐	☒
$B\,\overline{C}\,\overline{D}$	☐	☒
$A\,B\,\overline{D}$	☐	☒
$A\,C\,\overline{D}$	☐	☒
	☐	☐
	☐	☐

simplified SOP expression $Out = \overline{B}\cdot C + \overline{A}\cdot\overline{C}\cdot\overline{D} + A\cdot B\cdot\overline{D}$

Part A (12 points) For the follow expression, derive a simplified ***product of sums*** expression using a Karnaugh Map. Circle and list all prime implicants, indicating which are essential.

$$Out = (A + B + C)\cdot(\overline{B} + C + \overline{D})\cdot(A + \overline{B} + D)\cdot(\overline{A} + B + C + \overline{D})$$

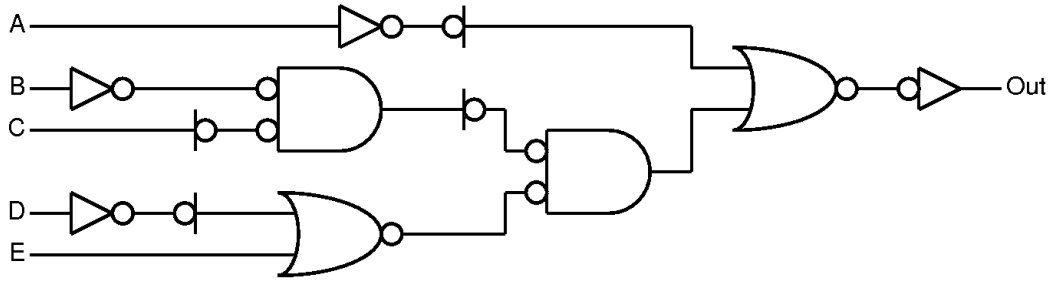


prime implicants	essential? yes	no
$A + C$	☒	☐
$C + \overline{D}$	☒	☐
$A + \overline{B} + D$	☒	☐
	☐	☐
	☐	☐
	☐	☐
	☐	☐
	☐	☐

simplified POS expression $Out = (A + C)\cdot(C + \overline{D})\cdot(A + \overline{B} + D)$

Problem 4 (3 parts, 26 points) Mixed Logic Design

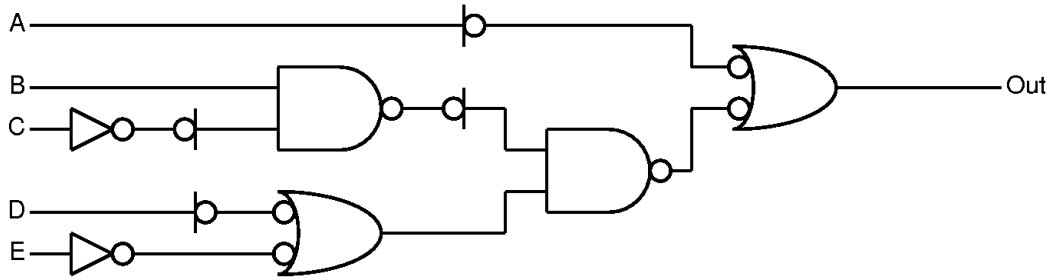
Part A (8 points) The following design has no supporting documentation. Derive the desired Boolean expression and the implementation cost (in CMOS transistors).



Out = $\overline{A} + \overline{B \cdot \overline{C}} \cdot (\overline{D} + E)$

transistors = 4 x 4 + 4 x 2 = 24 transistors

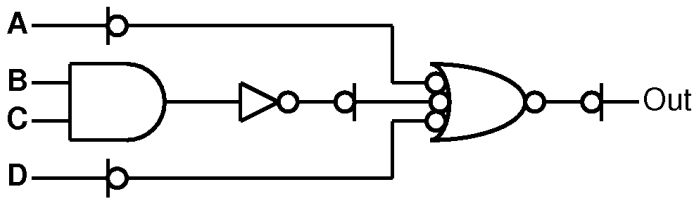
Part B (10 points) Now reimplement this expression using NAND gates and inverters. Use proper mixed logic notation. Determine the cost of this implementation (in transistors).



transistors = 4 x 4 + 2 x 2 = 20 transistors

Part C (8 points) Implement the following expression using only AND and NOT gates. Use proper mixed logic notation. Determine the cost of this implementation (in transistors).

$$Out = \overline{\overline{A} + B \cdot C + \overline{D}}$$



transistors = 1 x 6 + 1 x 8 + 1 x 2 = 16 transistors